

Monolithic integration of perovskites on Ge(001) by atomic layer deposition: a case study with $\text{SrHf}_x\text{Ti}_{1-x}\text{O}_3$

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(Received 12 July 2016; accepted 25 August 2016)

Abstract

This work reports the growth of crystalline $\text{SrHf}_x\text{Ti}_{1-x}\text{O}_3$ (SHTO) films on Ge (001) substrates by atomic layer deposition. Samples were prepared with different Hf content x to explore if strain, from tensile ($x=0$) to compressive ($x=1$), affected film crystallization temperature and how composition affected properties. Amorphous films grew at 225 °C and crystallized into epitaxial layers at annealing temperatures that varied monotonically with composition from ~530 °C ($x=0$) to ~660 °C ($x=1$). Transmission electron microscopy revealed abrupt interfaces. Electrical measurements revealed 0.1 A/cm² leakage current at 1 MV/cm for $x=0.55$.

Introduction

As the transistor feature sizes continue to scale ever smaller there has been a transition away from the use of conventional materials for the channel, Si, and the gate oxide, SiO_2 .^[1] Germanium has electron and hole mobility of 3900 and 1900 cm²/Vs, respectively, compared with 1400 and 470 cm²/Vs at 300 K, respectively, in Si. For this reason, Ge is being considered for p -type metal-oxide semiconductor field effect transistors (MOSFETs).^[1,2] There are multiple considerations in selecting a gate oxide material including the dielectric constant, band offset, leakage current, interface trap density (D_{it}), and ease of manufacturing.^[3,4] Various groups have reported gate oxides on Ge in MOSFETs, including $\text{TiO}_2/\text{Al}_2\text{O}_3$, ZrO_2 , LaAlO_3 on an interfacial layer of SrGe_x , HfO_2 on an interfacial layer of Y_2O_3 -doped GeO_2 , Y_2O_3 on a GeO_x interfacial layer, and HfO_2 with Al_2O_3 to suppress HfO_2 - GeO_x intermixing.^[5–10]

Amorphous oxides generally have lower dielectric constants than the crystalline form. However, the absence of grain boundaries in amorphous films is a potential advantage as grain boundaries can serve as defect trap sites.^[11] Crystalline oxides have been reported on silicon^[12–14] and germanium.^[13,15]

These crystalline oxides on semiconductors (COS) can offer high dielectric constants, perfection of the crystal structure at the oxide/semiconductor interface, and the possibility to coherently bond across the interface and minimize dangling bonds.^[12,13,16] Many COS are grown using molecular beam epitaxy (MBE). We recently reported an all-chemical growth process for SrTiO_3 (STO) and SrHfO_3 (SHO) on Ge (001) using atomic layer deposition (ALD) that illustrates a potentially scalable integration route to crystalline oxides on germanium.^[17,18]

SHO has a large band gap of 6.1 eV with favorable conduction band offset (~2.2 eV) and valence band offset (~3.2 eV) with Ge.^[19,20] This is in contrast with Ti-based perovskites, where the Ti 3d states yield negligible conduction band offsets with Si and Ge (~0.1–0.5 eV).^[21–23] Incorporation of Hf into the $\text{SrHf}_x\text{Ti}_{1-x}\text{O}_3$ alloy provides an upward shift of the d -states, which improves the conduction band offset, and increases the lattice constant, which may affect epitaxy. The dielectric constants were $k \sim 90$ and $k \sim 20$ for thin films of STO and SHO, respectively. Capacitor structures showed the leakage current for STO was around 10 A/cm² at 0.7 MV/cm with equivalent oxide thickness (EOT) of 0.7 nm and the leakage current for SHO was less than 10⁻⁵ A/cm² at 1.0 MV/cm with EOT of 1.0 nm.^[17,18]

In the previous studies amorphous films were deposited during ALD and the crystalline films formed after annealing at temperatures from 530 to 660 °C. The present study was

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undertaken to explore the role of strain on the crystallization temperature and composition on the film properties by growing alloys of $\text{SrHf}_x\text{Ti}_{1-x}\text{O}_3$ (SHTO) by ALD on Ge (001). The lattice constants of 3.905 Å and 4.069 Å for bulk STO and SHO, respectively, lead to 2.2% and -1.9% strain with the Ge (001) substrate for fully-strained, commensurate films affording a composition for which the lattice constant will match Ge (001) surface spacing along the [110] direction.

Experiment

The Ge substrates ($18 \times 20 \text{ mm}^2$) are diced from a 4-in Ge wafer (n-type, Sb-doped, 0.029–0.054 Ω·cm resistivity from MTI Corp.) The sample preparation procedure and experimental system are described in previous work.^[17,18] The wafer pieces are cleaned with acetone, isopropyl alcohol, and deionized water in an ultrasonic bath for 10 min each. After drying with nitrogen the sample is exposed to UV/ozone for 30 min to remove residual carbon contamination. The sample is mounted on a molybdenum puck and loaded into the vacuum system immediately and transferred to a MBE chamber. The sample is annealed and deoxidized in vacuum ($<2 \times 10^{-9}$ Torr) by heating from 200 to 500 °C at 20 °C/min and then from 550 to 650 °C at 10 °C/min, annealed at 650 °C for 1 h, and finally cooled to 200 °C at 30 °C/min. This procedure produces the 2×1 -reconstructed clean Ge (001) surface, which is essential as the starting surface for perovskite ALD.^[17,18] In situ reflection high-energy electron diffraction (RHEED) is used to verify the surface order; Supplementary Material Fig. S1 shows a representative surface after this procedure.

The Ge substrate with the 2×1 -reconstructed surface is transferred in situ to the ALD chamber^[24] where it is allowed to equilibrate for ~15 min at the growth temperature of 225 °C. Film growth is performed at 1 Torr using strontium bis(triisopropylcyclopentadienyl) [$\text{Sr}(\text{Pr}_3\text{Cp})_2$] (HyperSr), hafnium formamidate [$\text{Hf}(\text{fmd})_4$] (Hf-FAMD), titanium tetraisopropoxide [$\text{Ti}(\text{O}^i\text{Pr})_4$] (TTIP), and water as the co-reactant (oxygen source).^[17,18] The Sr, Hf, and Ti precursors were heated to 130, 115, and 40 °C, respectively. The quaternary compound was grown using the dosing and purging times indicated in Supplementary Material Fig. S2. For each metalorganic precursor, a 2-s dose time saturates the surface,^[17,18] the co-reactant water is dosed for 1 s. Following each precursor or water dose, a 15-s Ar purge is required. Our previous work showed that excess Sr was required to initiate the growth of STO on Ge. For this reason the Sr:Ti cycle ratio is 2:1^[18] and we adopted that protocol herein. Similarly, SHO growth on Ge used a Sr:Hf cycle ratio of 1:1 and that was used herein.^[17] The cycle ratios in Fig. S2 produce SHTO films that are slightly Sr-rich ($\text{Sr}/(\text{Sr} + \text{Hf} + \text{Ti})$) and a 1:1 (Sr:Ti + Hf) film requires some of the Sr:Ti cycle ratios to be 1:1 rather than 2:1. The quaternary $\text{SrHf}_x\text{Ti}_{1-x}\text{O}_3$ oxide was grown by adjusting the number of m SHO subcycles and n STO subcycles to vary the Hf content (x). Different film thickness is controlled by the total number of supercycles l .

Following ALD growth, the sample is transferred back to the MBE chamber and annealed while monitoring the surface in real

time with RHEED to follow the transformation from amorphous to crystalline. The substrate is heated from 200 to 500 °C with a 20 °C/min ramp rate, followed by a ramp rate of 10 °C/min as the temperature is increased further. Fig. S3 in Supplementary Material shows a representative transformation for an 11.4-nm $\text{SrHf}_{0.34}\text{Ti}_{0.66}\text{O}_3$ film. The temperature at which spots in the RHEED pattern emerge is monitored and this is referred to as the crystallization temperature. The temperature is then increased by 20 °C and held at this as annealing temperature for 5 min to fully crystallize the sample. The sample is then cooled to 200 °C at 30 °C/min and transferred from the MBE chamber.

The films were characterized by in situ x-ray photoelectron spectroscopy (XPS) to analyze the composition and uniformity using monochromatic Al $K\alpha$ source at 1486.6 eV and a VG Scienta R3000 analyzer, which is calibrated by a silver foil. High-resolution spectra are measured five times and summed up for the Sr ($3d$, $3p$), Ti ($2p$), O ($1s$), C ($1s$), Hf ($4f$), and Ge ($3d$) features. The measurement settings were 50 meV steps with 157 ms/step dwell time and 100 eV pass energy with a 0.4 mm analyzer slit width, which resulted in 350 meV effective resolution. The stoichiometry and Hf content x , which is defined as the ratio of Hf to (Hf + Ti), for the $\text{SrHf}_x\text{Ti}_{1-x}\text{O}_3$ films are calculated by the integrated area of the Sr $3d$, Ti $2p$, and Hf $4f$ peaks. The atomic sensitivity factors for Sr $3d$, Ti $2p$, and Hf $4f$ are set as 1.843, 2.001, and 2.639, respectively.^[25]

The thickness and crystallinity of the SHTO films were measured by x-ray reflectivity (XRR) and x-ray diffraction (XRD) on a Rigaku Ultima IV system with a Cu $K\alpha$ source. The interface of selected samples was examined by cross-sectional transmission electron microscopy (TEM). The samples were prepared via standard cross-section method with Ar ion milling. Aberration-corrected scanning transmission electron microscopy (STEM) was used for further interface study. The TEM images were taken with a JEOL 2010F and STEM images were taken with a JEOL ARM 200F. Electron-energy-loss spectroscopy (EELS) composition mapping was also applied to investigate the elemental distributions in the growth direction.

Electrical properties (dielectric constant k and leakage current I) were established for some samples by fabricating standard metal oxide semiconductor capacitor (MOS capacitor) structures. The films had a top electrode of TaN applied by sputtering and 15 μm photolithographic features were defined with a SF_6 -based plasma etch. After building up the MOS capacitor structure the back side of the wafer was scratched and silver paste was applied to form the bottom electrode. The capacitance–voltage (C – V) and current–voltage (I – V) measurements were performed on an Agilent B1500A semiconductor device parameter analyzer with a Cascade Microtech probe station.

Results and Discussion

Deposition and crystallization of $\text{SHf}_x\text{Ti}_{1-x}\text{O}_3$ films

Films with Hf content x distributed from 0 to 1 and with thickness between 8.0 and 14.0 nm were grown. SHTO films were deposited with subcycle ratios (Fig. S2) $m:n$ from 1:3 to 7:1.

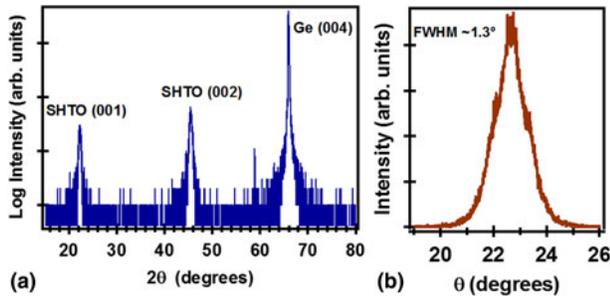


Figure 1. X-ray diffraction pattern (a), and rocking curve (b), for a 13.9-nm $\text{SrHf}_x\text{Ti}_{1-x}\text{O}_3$ ($x=0.47$) film grown on Ge (001) by ALD, and annealed at 632°C for 5 min in vacuum. The peak of the SHTO (002) reflection is at $2\theta = 45.45 \pm 0.5^\circ$ and the rocking curve for the (002) reflection has a full width at half maximum (FWHM) of 1.3° .

STO ($x=0$) and SHO ($x=1$) films were deposited with around 100 cycles producing films that were 9.7 and 12.6 nm thick, respectively. SHTO films were grown with a total of $l \times (2m + 3n)$ cycles as the composition was varied.

The XRD and rocking curve measurements confirmed the crystallization that was indicated by RHEED. Figure 1 shows the θ - 2θ XRD and rocking curve around the film (002) reflection at $2\theta = 45.45 \pm 0.5^\circ$ for a film with a composition of $\text{SrHf}_{0.47}\text{Ti}_{0.53}\text{O}_3$. This sample was grown with a $m:n = 2:1$ sub-cycle ratio and $l = 14$ supercycles. The crystallization temperature was found to be 612°C . Ex situ XRR measurement indicates the thickness is 13.9 nm. The (002) reflection at $2\theta = 45.45 \pm 0.5^\circ$ leads to an out-of-plane lattice constant of $c = 3.989 \pm 0.005 \text{ \AA}$. The rocking curve scan around the SHTO (002) reflection reveals a full-width half-maximum (FWHM) of 1.3° . The best quality STO and SHO films grown by ALD had FWHM of 0.8° and 1.2° , respectively.^[17,18]

Figure 2(a) illustrates that the temperature for crystallization onset increases monotonically with increasing Hf content from

510°C for STO to 640°C for SHO. The bulk lattice constants for SHO and STO are 4.069 \AA and 3.905 \AA , respectively. By assuming SHTO forms a substitutional alloy, and by applying Vegard's law the bulk SHTO lattice constants (a) can be estimated as a function of x and these are represented by the red line in Fig. 2(b). From Ge surface spacing of 3.992 \AA along the $\langle 110 \rangle$ direction we compute the in-plane strain that should result from a fully-strained, commensurate film at room temperature and present this as the green line in Fig. 2(b). The fully-strained films vary from tensile (2.2% for $x=0$) to compressive (-1.9% for $x=1$). At room temperature a value of $x \sim 0.53$ should give zero strain.

The coefficient of thermal expansion for Ge is approximately one order of magnitude less than a composition-averaged value for SHTO alloys^[26–28]; over the 500 – 650°C annealing window the x -value that matches the Ge separation distance decreases from $x \sim 0.4$ to $x \sim 0.3$. We sought to determine how interface strain influenced crystallization. The monotonically increasing crystallization temperature with x [Fig. 2(a)] suggests that the annealing temperature does not depend on the strain between the substrate and the SHTO alloy but is rather dependent on the atomic mass of the elements that comprise the alloy. Since the atomic mass of Hf is much heavier than Ti, it will require more thermal energy to move Hf to the correct perovskite crystal structure location compared with Ti, similar to what has been found for A-site cations in ATiO_3 perovskites.^[29]

Both Sr-rich or Sr-lean stoichiometry in STO films grown on STO produced an out-of-plane lattice constant that was greater than expected for a fully strained film.^[30] Similar results are reported herein for SHTO alloys. The SHTO out-of-plane lattice constants c are determined from the (002) XRD reflections and are also presented in Fig. 2(b). In general the experimental c -values are greater than a [the red line in Fig. 2(b)] for compressive films and less than a for tensile films, consistent with expectations for commensurate films. The squares

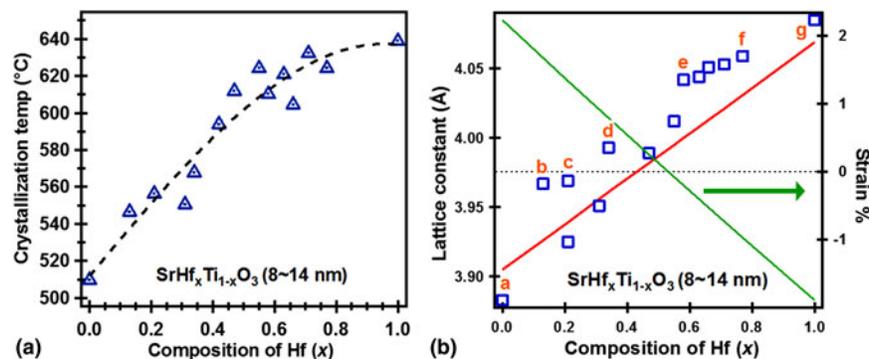


Figure 2. (a) Crystallization temperature T versus Hf content x for 8–14-nm thick $\text{SrHf}_x\text{Ti}_{1-x}\text{O}_3$ films. The dashed line is drawn to guide the eye. (b) Predicted dependence of the bulk lattice constant and film with Hf content x at room temperature. The red line indicates the cubic lattice constant a of bulk $\text{SrHf}_x\text{Ti}_{1-x}\text{O}_3$ based on Vegard's law. The green line presents the strain for commensurate and fully-strained SHTO films on Ge. The squares are the experimental out-of-plane lattice constants c for different x ; the squares with letters indicate that samples with Sr-rich compositions of around 55% and other squares indicate samples for which the Sr composition varied from 49% to 51%.

with letters in Fig. 2(b) correspond to samples that have Sr-enrichment around 55% and could be expected to have out-of-plane lattice constants that are greater than fully-strained stoichiometric films. A fully-strained stoichiometric STO film on Ge should have an out-of-plane lattice constant of 3.852 Å at room temperature based on a Possion ratio for STO of 0.232.^[31] The experimental STO value [Square (a) in Fig. 2(b)] is 3.883 Å due to Sr-enrichment. Similarly Squares (b), (c), and (d) correspond to samples with out-of-plane lattice constants that are greater than the bulk, cubic lattice constant. Whereas, films with similar compositions as (b), (c), and (d), which should be under tension if fully strained, display out-of-plane lattice constants less than a .

In situ XPS study and composition uniformity

In situ XPS was performed on the SHTO films before and after annealing. Figure 3 presents results for a SrHf_{0.56}Ti_{0.44}O₃ film grown with an $m:n = 3:1$ subcycle ratio and $l = 11$ supercycles. This film started to crystallize at 633 °C had an out-of-plane lattice constant of 4.04 Å. Figures 3(a)–3(e) present the Sr 3*d*, Ti 2*p*, O 1*s*, C 1*s* + Sr 3*p*, and Hf 4*f* core levels, respectively. The Sr 3*d*_{3/2} and 3*d*_{5/2} peaks are located at binding energies of 135.5 eV and 133.8 eV, respectively, which indicates that the Sr is fully oxidized (Sr²⁺) in the SHTO film.^[32] Similarly, the Ti 2*p* and Hf 4*f* features in Figs. 3(b) and 3(e), respectively, correspond to fully oxidized Ti (Ti⁴⁺) and Hf (Hf⁴⁺). Figure 3(d) shows that there is no carbon peak at the C 1*s* position of 285 eV.

The Hf content $x = 0.56$ and stoichiometry are determined by integrating the areas of the Sr 3*d*, Ti 2*p*, and Hf 4*f* features. The Sr:(Hf + Ti) ratios for films in this study reveal a stoichiometry that is consistent with an ABO₃ perovskite. Previous work in our group has shown that slightly Sr-rich films (i.e., A-rich) crystallize more readily on Ge (001) than B-rich films.^[17,18] The Sr composition (viz., Sr/(Sr + Hf + Ti)) of all crystallized films falls in the range from 49% to 56%. Some films outside this range, such as 47% Sr, are still observed to crystallize. However, the RHEED images for such films (not shown) suggest rough surfaces and imply lower crystalline quality. The sample in Fig. 3 has the ratio of A:B of 55:45. The value of $x = 0.56$ for a $m:n = 3:1$ subcycle ratio suggests that Ti is more readily incorporated into the SHTO alloy during ALD.

Compositional uniformity across the Ge substrates and throughout the films was probed with XPS. Figure S4 in Supplementary Material presents results for the film discussed in Fig. 3 after annealing at 653 °C for 5 min. The composition is uniform across the film and constant with gas flow direction, as would be expected for an ALD process. Angle-resolved XPS (AR-XPS) was performed at wafer position Number 5 to examine uniformity of the composition through the thickness of the film. Based on the universal escape depth curve^[33] we estimate nearly 97% of the signal comes from a depth of no more than ~6.2 nm from the SHTO surface when the sample is fixed on the horizontal plane (i.e., 0°). The analysis angles [Fig. S4 (b)] of 0°, 15°, 30°, 45°, and 60° correspond to sampling depths of 6.2, 6.0, 5.4, 4.4, and 3.1 nm, respectively. For the

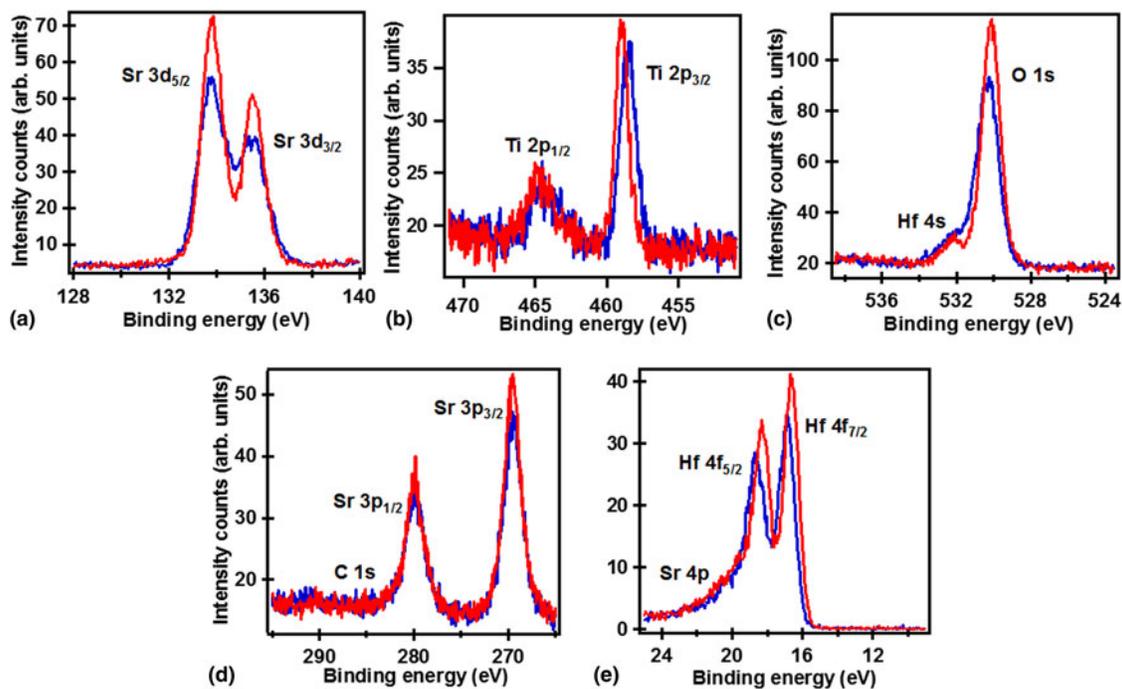


Figure 3. X-ray photoelectron spectra for Sr 3*d* (a), Ti 2*p* (b), O 1*s* (c), C 1*s* and Sr 3*p* (d), and Hf 4*f* (e) in a SrHf_{0.56}Ti_{0.44}O₃ ($x = 0.56$) film grown by ALD on Ge (001). The blue line in each figure corresponds to the spectrum post-deposition and the red line corresponds to the spectrum after annealing at 654 °C for 5 min.

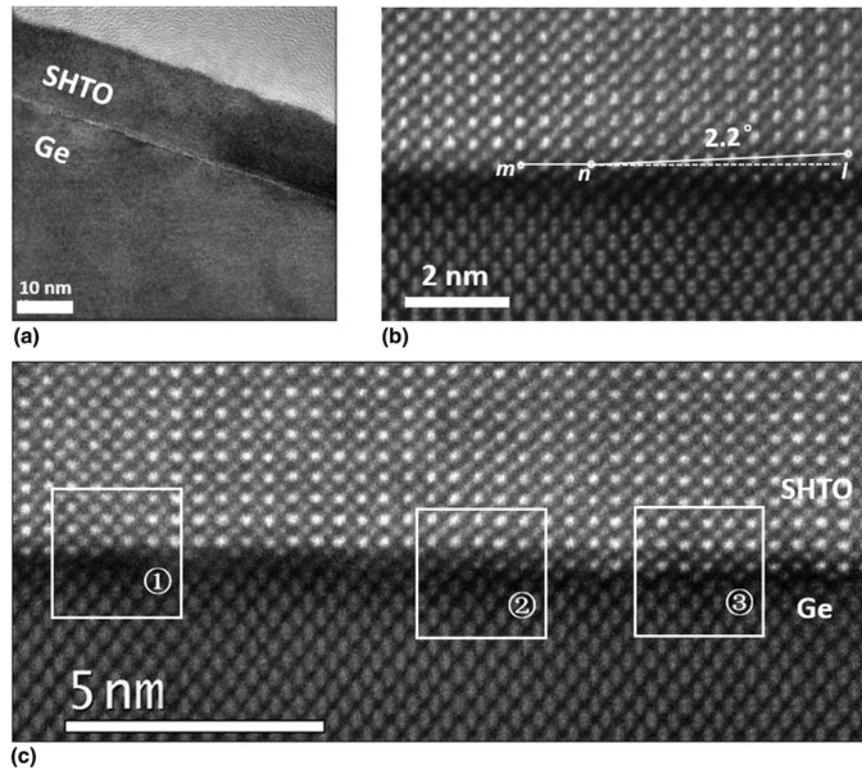


Figure 4. Transmission electron micrographs of 14.0-nm $\text{SrHf}_{0.55}\text{Ti}_{0.45}\text{O}_3$ with post-deposition annealing at 645 °C for 5 min: (a) TEM image showing the cross-sectional view of the SHTO film, (b) and (c) high-resolution HAADF STEM images showing details of the SHTO–Ge interface. In Figure (b), the full lines mn and nl indicate the Hf/Ti plane, the dotted line is the extension of mn . The included angle between nl and the dotted line is $\sim 2.2^\circ$. Regions ①, ②, and ③ in Figure (c) are selected for further analysis in Fig. 5.

AR-XPS scans that sampled depths of 6.2–3.1 nm from the free surface, the Sr, Hf, and Ti compositions were $54.8 \pm 0.9\%$, $25.0 \pm 0.6\%$, and $20.3 \pm 0.6\%$, respectively, confirming the composition uniformity with depth of the film.

Microstructure

Previous work in our group showed that annealing temperature affected the interface trap density of SHO films. Films annealed at less than 650 °C retained an abrupt Ge–SHO interface. SHO annealed at 700 °C and 750 °C resulted in an interfacial layer evidenced by Ge XPS features that are suggestive of a hafnium germanide and a noncrystalline layer at the SHO–Ge interface.^[17] TEM and high-angle annular-dark-field (HAADF) analysis was performed on a 14-nm $\text{SrHf}_{0.55}\text{Ti}_{0.45}\text{O}_3$ sample, which was grown with a subcycle ratio $m:n = 3:1$ using 11 supercycles. For this sample the Sr:Ti dosing ratio was 1:1 within subcycle n and this produced a film with Sr/(Sr + Hf + Ti) of 49%. A film with good thickness uniformity and surface smoothness is visible in Fig. 4(a). Figures 4(b) and 4(c) show HAADF STEM images of the SHTO–Ge interface and Fig. 5 shows enlargements from selected regions in Fig. 4(c). An abrupt interface without an obvious interfacial layer is found when the SHTO was annealed at 645 °C, which is consistent with previous work.^[17] EELS composition mapping was

performed to investigate the element distribution across the interface. The EELS data presented in Supplementary Material Fig. S5 indicate that there is no interdiffusion across the Ge–SHTO interface and that the SHTO composition is spatially uniform.

Local grain tilting is apparent in Fig. 4(b). The solid lines mn and nl represent the Hf/Ti plane adjacent to the Ge (001) surface and the dotted line is an extension of line mn . The included angle indicates a local crystal tilt near the Ge surface of $\sim 2.2^\circ$. Grain tilting can be caused by several factors, including lattice mismatch and the resulting film strain.^[34,35] The $\text{SrHf}_{0.55}\text{Ti}_{0.45}\text{O}_3$ film is expected to be under compressive strain through the crystallization and cooling steps. During the transformation from amorphous into a crystalline film, nucleation likely occurs at the Ge–SHTO interface to produce the epitaxial film. The presence of a stepped surface with a step height of $\sim 5.658/4 = 1.414 \text{ \AA}$, as visible in Fig. 4(b) and Region 1 in Fig. 4(c), will impact the lateral area over which an epitaxial crystalline domain can grow before it encounters a step that is not commensurate with the SHTO step height.

An antiphase boundary (APB) is visible in Region 1 of Fig. 4(c) as illustrated in the enlargements in Figs. 5(a) and 5(d). One possible reason is that crystallites initiated on adjacent Ge regions extend laterally to intersect above the surface step

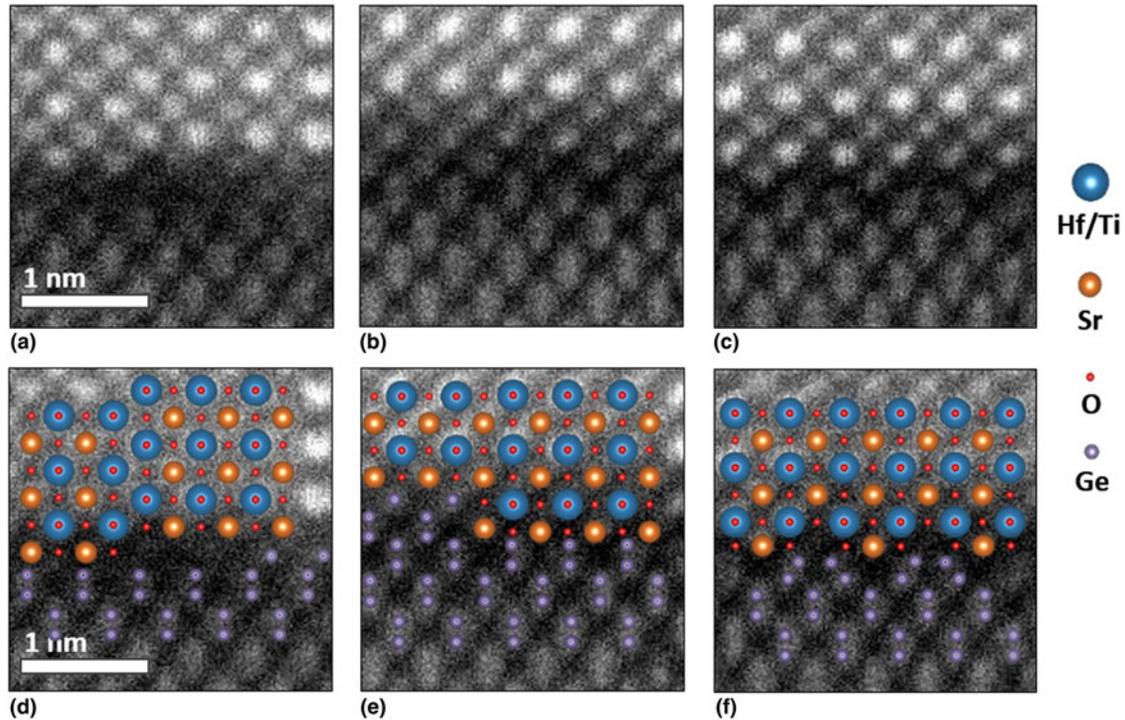


Figure 5. HAADF STEM images (a), (b), and (c) showing enlarged portions of regions ①, ②, and ③ in Fig. 4(c), respectively. (d), (e), and (f) HAADF STEM images with atomic structure overlaid for (a), (b), and (c), respectively.

leading to APB formation in the crystalline film.^[36] The Ge columns near the APB in Figs. 5(a) and 5(d) are misaligned with the Hf/Ti columns, which is consistent with APB observed for STO grown by MBE on Si(001).^[36] Since the misalignments can result in increased Ge–Sr bond length, dangling Ge bonds may also be formed. Both of the column misalignments and the possible Ge dangling bonds at the interface may contribute to the electrical trap states observed for SHO.^[17]

Surface steps without the formation of an APB are also indicated in Figs. 5(b) and 5(e).

The enlarged images in Figs. 5(c) and 5(f) indicate that some Ge retains the 2×1 reconstruction present in the starting surface (Fig. S1) during ALD and annealing. The local interface structure in Figs. 5(b)/5(e) and 5(c)/5(f) is similar to that found for BaTiO₃ deposited by MBE on a Ge (001) surface that first had $\frac{1}{2}$ -monolayer of Sr deposited to form a Zintl

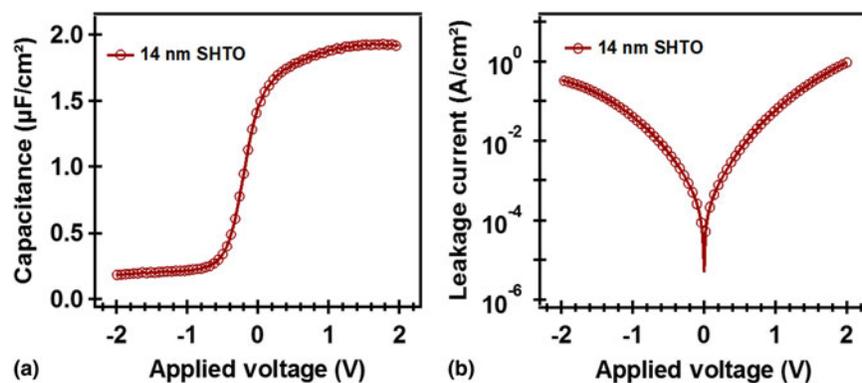


Figure 6. Capacitance–voltage (C – V) measurement (a) and current–voltage (I – V) measurement (b) for a 14.0-nm SrHf_{*x*}Ti_{1–*x*}O₃ ($x=0.55$) film on Ge. The dielectric constant is $k=30$ and the leakage current is 0.1 A/cm^2 at 1 MV/cm with an EOT = 1.8 nm .

layer.^[37] This might indicate that Sr orders at the Ge(001) surface during the initial ALD cycles to form a Zintl layer with Ge.

Electrical properties

The crystallization temperatures for 4-nm thick STO and SHO were around 510 and 640 °C, respectively.^[17,18] At 700 °C an interfacial layer forms at the Ge–SHO interface that is <0.5 nm thick. This interfacial layer may contribute to the D_{it} values that were as low as $2 \times 10^{12}/\text{cm}^2/\text{eV}$ for crystalline SHO and increased with higher annealing temperatures.^[17] To test the dielectric performance, the $\text{SrHf}_x\text{Ti}_{1-x}\text{O}_3$ film with $x=0.55$ was used to fabricate a MOS capacitor. The microstructure for this film revealed the presence of an APB in the near surface region and partial 2×1 reconstruction at the Ge–SHTO interface and these could be sources of interface trap states. The C – V and I – V curves are shown in Figs. 6(a) and 6(b), respectively. From Fig. 6(a), the capacitance achieves saturation at around $1.93 \mu\text{F}/\text{cm}^2$, corresponding to a relative dielectric constant of $k \sim 30$. The I – V measurement shows leakage current of $0.1 \text{ A}/\text{cm}^2$ at $1 \text{ MV}/\text{cm}$ with an EOT = 1.8 nm. The leakage current for SHO is $6.3 \times 10^{-6} \text{ A}/\text{cm}^2$ at $1 \text{ MV}/\text{cm}$ with an EOT = 1.0 nm and for STO is $10 \text{ A}/\text{cm}^2$ at $1 \text{ MV}/\text{cm}$ with an EOT = 0.7 nm. Ignoring the EOT difference, which will cause even higher leakage current for this $\text{SrHf}_{0.55}\text{Ti}_{0.45}\text{O}_3$ sample if it was considered, the leakage current of SHTO shows two orders of magnitude improvement over STO. However, the leakage current was too high to allow reliable D_{it} to be measured.

Conclusion

In this study, amorphous $\text{SrHf}_x\text{Ti}_{1-x}\text{O}_3$ films have been grown on Ge (001) by ALD and annealed at 530 °C to 660 °C to crystallize the film into a heteroepitaxial layer. Matching the lattice constant for the SHTO alloy with the substrate by adjusting x did not lower the SHTO crystallization temperature rather the crystallization temperature increased monotonically with increasing Hf content. In situ XPS and AR-XPS analysis verified the stoichiometry and uniformity of the grown films. The ex situ XRR, XRD and rocking curve measurements were used to determine the thickness, lattice constant and crystallinity, respectively. The interface of a selected sample was explored by STEM and EELS mapping. The interface was abrupt with no apparent interdiffusion between the substrate and the SHTO layer. Regions of the Ge surface retained the 2×1 reconstruction. Film defects included grain tilting and APBs that are attributed to steps at the Ge (001) surface. Capacitance–voltage (C – V) and current–voltage (I – V) measurements show the dielectric constant $k=30$ and a leakage current $I=0.1 \text{ A}/\text{cm}^2$ (at $1 \text{ MV}/\text{cm}$ with EOT = 1.8 nm) after the standard MOS capacitor was fabricated from a 14.0-nm SHTO film with Hf content $x=0.55$.

These results for SHTO films illustrate that ALD growth of quaternary perovskites directly on Ge (001) is feasible and that the properties can be adjusted with B-site substitution. Understanding the influence of strain and film microstructure

on the D_{it} for crystalline ABO_3 layers on Ge necessarily requires systems that do not feature Ti on the B-site since Ti $3d$ states yield negligible conduction band offset with Ge, and require systems that crystallize at or below 650 °C to maintain an abrupt interface.

Supplementary Material

The supplementary material for this article can be found at <http://dx.doi.org/10.1557/mrc.2016.36>

Acknowledgments

This research was supported by the National Science Foundation (Award CMMI-1437050) and the Air Force Office of Scientific Research (Grant FA9550-14-1-0090).

References

1. P.S. Goley and M.K. Hudait: Germanium based field-effect transistors: challenges and opportunities. *Materials* **7**, 2301–2339 (2014).
2. E. Simoen, J. Mitard, G. Hellings, G. Eneman, B. De Jaeger, L. Witters, B. Vincent, R. Loo, A. Delabie, S. Sioncke, M. Caymax, and C. Claeys: Challenges and opportunities in advanced Ge pMOSFETs. *Mater. Sci. Semicond. Process.* **15**, 588–600 (2012).
3. Y. Kamata: High-k/Ge MOSFETs for future nanoelectronics. *Mater. Today* **11**, 30–38 (2008).
4. G.D. Wilk, R.M. Wallace, and J.M. Anthony: High-k gate dielectrics: current status and materials properties considerations. *J. Appl. Phys.* **89**, 5243–5275 (2001).
5. S. Swaminathan, M. Shandalov, Y. Oshima, and P.C. McIntyre: Bilayer metal oxide gate insulators for scaled Ge-channel metal-oxide-semiconductor devices. *Appl. Phys. Lett.* **96**, 082904 (2010).
6. C.O. Chui, S. Ramanathan, B.B. Triplett, P.C. McIntyre, and K.C. Saraswat: Germanium MOS capacitors incorporating ultrathin high-n gate dielectric. *IEEE Electron Device Lett.* **23**, 473–475 (2002).
7. Y. Kamata, K. Ikeda, Y. Kamimuta, and T. Tzuka: High-k/Ge p- & n-MISFETs with strontium germanide interlayer for EOT scalable CMOS application. *2010 Symposium on VLSI Technology. IEEE*, 2010; pp. 211–212.
8. C.H. Lee, C. Lu, T. Tabata, W.F. Zhang, T. Nishimura, K. Nagashio, and A. Toriumi: Oxygen potential engineering of interfacial layer for deep sub-nm EOT high-k gate stacks on Ge. *Electron Devices Meeting. IEEE*, 2013; pp. 2.5.1–2.5.4.
9. C.H. Lee, C. Lu, T. Tabata, T. Nishimura, K. Nagashio, and A. Toriumi: Enhancement of high-Ns electron mobility in sub-nm EOT Ge n-MOSFETs. *2013 Symposium on VLSI Technology. IEEE*, 2013; pp. T28–T29.
10. R. Zhang, P.C. Huang, N. Taoka, M. Takenaka, and S. Takagi: High mobility Ge pMOSFETs with 0.7 nm ultrathin EOT using $\text{HfO}_2/\text{Al}_2\text{O}_3/\text{GeO}_2/\text{Ge}$ gate stacks fabricated by plasma post oxidation. *2012 Symposium on VLSI Technology. IEEE*, 2012; pp. 161–162.
11. A. Rollett, F.J. Humphreys, G.S. Rohrer, and M. Hatherly: *Recrystallization and Related Annealing Phenomena* (Elsevier, 2004); pp. 151.
12. J.W. Reiner, A.M. Kolpak, Y. Segal, K.F. Garrity, S. Ismail-Beigi, C.H. Ahn, and F.J. Walker: Crystalline oxides on silicon. *Adv. Mater.* **22**, 2919–2938 (2010).
13. R.A. McKee, F.J. Walker, and M.F. Chisholm: Physical structure and inversion charge at a semiconductor interface with a crystalline oxide. *Science* **293**, 468–471 (2001).
14. R.A. McKee, F.J. Walker, and M.F. Chisholm: Crystalline oxides on silicon: the first five monolayers. *Phys. Rev. Lett.* **81**, 3014–3017 (1998).
15. M. Jahangir-Moghadam, K. Ahmadi-Majlan, X. Shen, T. Droubay, M. Bowden, M. Chrysler, D. Su, S.A. Chambers, and J.H. Ngai: Band-gap engineering at a semiconductor-crystalline oxide interface. *Adv. Mater. Interfaces* **2**, 1400497 (2015).

16. A.A. Demkov, A.B. Posadas, H. Seo, M. Choi, K.J. Kormondy, P. Ponath, R.C. Hatch, M.D. McDaniel, T.Q. Ngo, and J.G. Ekerdt: Monolithic integration of oxides on semiconductors. *ECS Trans.* **54**, 255–269 (2013).
17. M.D. McDaniel, C. Hu, S. Lu, T.Q. Ngo, A. Posadas, A. Jiang, D.J. Smith, E.T. Yu, A.A. Demkov, and J.G. Ekerdt: Atomic layer deposition of crystalline SrHfO₃ directly on Ge (001) for high-k dielectric applications. *J. Appl. Phys.* **117**, 54101 (2015).
18. M.D. McDaniel, T.Q. Ngo, A. Posadas, C. Hu, S. Lu, D.J. Smith, E.T. Yu, A. Demkov, and J.G. Ekerdt: A chemical route to monolithic integration of crystalline oxides on semiconductors. *Adv. Mater. Interfaces* **1**, 1400081 (2014).
19. I. Oh, M.-K. Kim, J. Lee, C.-W. Lee, C. Lansalot-Matras, W. Noh, J. Park, A. Noori, D. Thompson, S. Chu, W.J. Maeng, and H. Kim: The effect of La₂O₃-incorporation in HfO₂ dielectrics on Ge substrate by atomic layer deposition. *Appl. Surf. Sci.* **287**, 349–354 (2013).
20. L. Bjaalie, B. Himmetoglu, L. Weston, A. Janotti, and C.G.V. de Walle: Oxide interfaces for novel electronic applications. *New J. Phys.* **16**, 025005 (2014).
21. S.A. Chambers, Y. Liang, Z. Yu, R. Droopad, and J. Ramdani: Band offset and structure of SrTiO₃/Si (001) heterojunctions. *J. Vac. Sci. Technol. A* **19**, 934–939 (2001).
22. F. Amy, A.S. Wan, A. Kahn, F.J. Walker, and R.A. McKee: Band offsets at heterojunctions between SrTiO₃ and BaTiO₃ and Si (100). *J. Appl. Phys.* **96**, 1635–1639 (2004).
23. X. Zhang, A.A. Demkov, H. Li, X. Hu, Y. Wei, and J. Kulik: Atomic and electronic structure of the Si/SrTiO₃ interface. *Phys. Rev. B* **68**, 125323 (2003).
24. M.D. McDaniel, A. Posadas, T. Wang, A.A. Demkov, and J.G. Ekerdt: Growth and characterization of epitaxial anatase TiO₂(001) on SrTiO₃-buffered Si(001) using atomic layer deposition. *Thin Solid Films* **520**, 6525–6530 (2012).
25. C.D. Wagner, W.M. Riggs, L.E. Davis, and J.F. Moulder: *Handbook of X-ray Photoelectron Spectroscopy*, edited by G.E. Muilenberg (Perkin-Elmer, Physical Electronics Division, Eden Prairie, MN, 1979); pp. 253.
26. D. de Ligny and P. Richet: High-temperature heat capacity and thermal expansion of SrTiO₃ and SrZrO₃ perovskites. *Phys. Rev. B* **53**, 3013–3022 (1996).
27. H.P. Singh: Determination of thermal expansion of germanium, rhodium and iridium by X-rays. *Acta Crystallogr. Sect. A* **24**, 469–471 (1968).
28. S. Yamanaka, T. Maekawa, H. Muta, T. Matsuda, S. Kobayashi, and K. Kurosaki: Thermophysical properties of SrHfO₃ and SrRuO₃. *J. Solid State Chem.* **177**, 3484–3489 (2004).
29. A. Meldrum, L.A. Boatner, W.J. Weber, and R.C. Ewing: Amorphization and recrystallization of the ABO₃ oxides. *J. Nucl. Mater.* **300**, 242–254 (2002).
30. C.M. Brooks, L.F. Kourkoutis, T. Heeg, J. Schubert, D.A. Muller, and D.G. Schlom: Growth of homoepitaxial SrTiO₃ thin films by molecular-beam epitaxy. *Appl. Phys. Lett.* **94**, 162905 (2009).
31. H. Ledbetter, M. Lei, and S. Kim: Elastic constants, debye temperatures, and electron-phonon parameters of superconducting cuprates and related oxides. *Phase Transit.* **23**, 61–70 (1990).
32. J.F. Moulder, W.F. Stickle, P.E. Sobol, and K.D. Bomben: *Handbook of X-ray Photoelectron Spectroscopy*, edited by J. Chastain (Perkin-Elmer, Physical Electronics Division, Eden Prairie, MN, 1993).
33. M.P. Seah and W.A. Dench: Quantitative electron spectroscopy of surfaces: a standard data base for electron inelastic mean free paths in solids. *Surf. Interface Anal.* **1**, 2–11 (1979).
34. X. Jiang, R.Q. Zhang, G. Yu, and S.T. Lee: Local strain in interface: origin of grain tilting in diamond (001)/silicon (001) heteroepitaxy. *Phys. Rev. B* **58**, 15351–15354 (1998).
35. C.M. Foster, Z. Li, M. Buckett, D. Miller, P.M. Baldo, L.E. Rehn, G.R. Bai, D. Guo, H. You, and K.L. Merkle: Substrate effects on the structure of epitaxial PbTiO₃ thin films prepared on MgO, LaAlO₃, and SrTiO₃ by metal-organic chemical-vapor deposition. *J. Appl. Phys.* **78**, 2607–2622 (1995).
36. H. Wu, T. Aoki, A.B. Posadas, A.A. Demkov, and D.J. Smith: Anti-phase boundaries at the SrTiO₃/Si(001) interface studied using aberration-corrected scanning transmission electron microscopy. *Appl. Phys. Lett.* **108**, 091605 (2016).
37. K.D. Fredrickson, P. Ponath, A.B. Posadas, M.R. McCartney, T. Aoki, D.J. Smith, and A.A. Demkov: Atomic and electronic structure of the ferroelectric BaTiO₃/Ge(001) interface. *Appl. Phys. Lett.* **104**, 242908 (2014).